

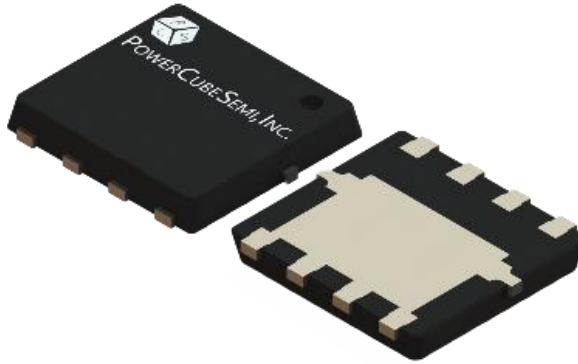
PM018P080GG

-80V -78A 18mΩ Si Single P-ch Enhancement Mode MOSFET with Normal Diode

Features

Si P-Ch Enhancement Mode Power MOSFET

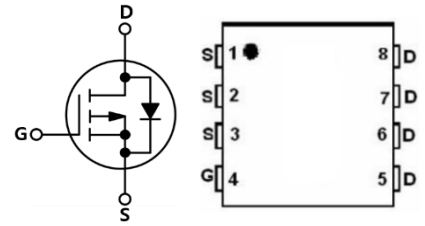
- Rated to -80V at -78Amps @ $T_j = 25^\circ\text{C}$
- Max $R_{DS(on)} = 18\text{ m}\Omega$
- Typ $R_{DS(on)} = 14\text{ m}\Omega$
- Gate Charge(Typ. $Q_g=62.1\text{ nC}$)
- 100% Avalanche Tested



PKG type : DFN5X6-8L

Application

- Power switch
- DC/DC converters



Description

The PM018P080GG uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

Absolute Maximum Ratings

Symbol	Parameter	Test Condition	Value	Unit
BV_{DSS}	Drain-source breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-80	V
I_D	Drain current	$T_c=25^\circ\text{C}$	-78	A
I_{DM}	Drain current	Pulse width limited by junction temperature	-312	A
V_{GS}	Gate-source voltage		± 20	V
E_{AS}	Single pulsed avalanche energy	$V_{GS}=-10V, R_G=25\Omega$ $V_{DD}=-50V, L=0.5mH$	272	mJ
P_d	Power dissipation	$T_c=25^\circ\text{C}$	178	W
T_j	Operating junction		150	$^\circ\text{C}$
T_{stg}	Storage temperature		-55 to 150	$^\circ\text{C}$

Package Marking and Ordering Information

Device Marking	Device	Package	Packing Method	Tape width	Quantity
PM018P080GG	PM018P080	DFN5X6-8L	REEL	-	5000

Electrical Characteristics of Si MOSFET

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
BV_{DSS}	Drain-source breakdown voltage	$V_{GS} = 0V, I_D = -250\mu A, T_J = 25^\circ C$	-80	-	-	V
I_{DSS}	Zero gate voltage drain current	$V_{DS} = -80V, V_{GS} = 0V$	-	-	-1	μA
I_{GSS}	Gate-source leakage current	$V_{GS} = \pm 20V$	-	-	± 100	nA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.8	-2.5	V
$R_{DS(on)}$	Static drain-source on state resistance	$V_{GS} = -10V, I_D = -20A$	-	14	18	m Ω
		$V_{GS} = -4.5V, I_D = -15A$	-	16	21	
g_{FS}	Forward transconductance	$V_{DS} = -5V, I_D = -20A$	-	46	-	S
$t_{d(on)}$	Turn-on Delay time	$V_{DD} = -40V, I_D = -20A, R_G = 3\Omega$	-	18	-	ns
t_r	Turn-on Rise time		-	20	-	
$t_{d(off)}$	Turn-off Delay time		-	55	-	
t_f	Turn-off Fall time		-	35	-	

Electrical Characteristics of Si MOSFET

Symbol	Parameter	Test Condition	Numerical		Unit
			Typ.	Max.	
$R_{\theta JC}$	Thermal resistance, Junction to case		0.7	-	$^{\circ}\text{C}/\text{W}$
C_{iss}	Input capacitance	$V_{DS} = -40\text{V}, V_{GS} = 0\text{V},$ $f = 1.0\text{MHz}$	5998	-	pF
C_{oss}	Output capacitance		458	-	
C_{rss}	Reverse transfer capacitance		17	-	
$Q_{g(\text{tot})}$	Total gate charge at 10V	$V_{DD} = -40\text{V}, I_D = -20\text{A}$ $V_{GS} = -10\text{V}$	62.1	-	nC
Q_{gs}	Gate to source gate charge		9.3	-	
Q_{gd}	Gate to drain "Miller" charge		16.8	-	

Electrical Characteristics of Si Diode

Symbol	Parameter	Test Condition	Numerical		Unit
			Typ.	Max.	
I_S	Maximum continuous drain to source diode forward current	$T_c = 25^{\circ}\text{C}$	-	-78	A
V_{SD}	Drain to source diode forward voltage	$I_{SD} = -20\text{A}, V_{GS} = 0\text{V}$	-	-1.2	V
T_{rr}	Reverse recovery time	$I_F = -20\text{A}, V_{GS} = 0\text{V},$ $di_F/dt = -100\text{A}/\mu\text{s}$	71	-	ns
Q_{rr}	Reverse recovery charge		49	-	nC

Typical Characteristics

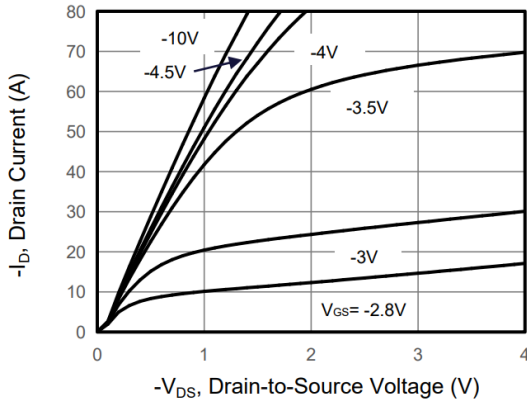


Figure 1. Output Characteristics

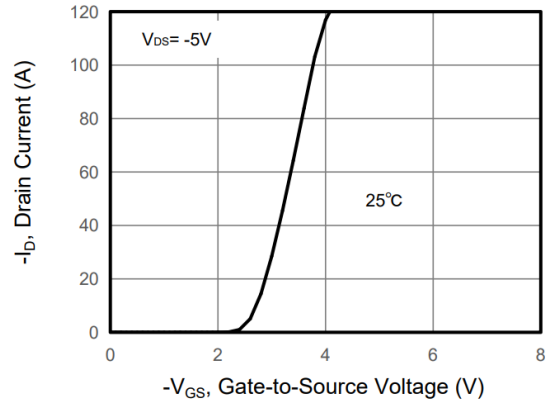


Figure 2. Transfer Characteristics

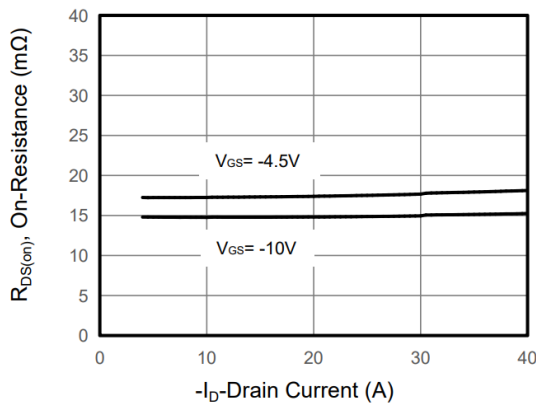


Figure 3. Drain Source On Resistance

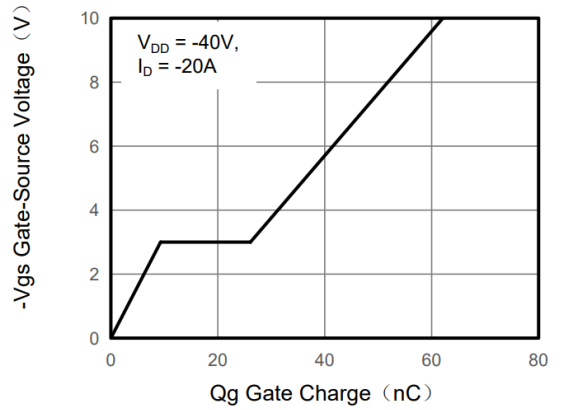


Figure 4. Gate Charge

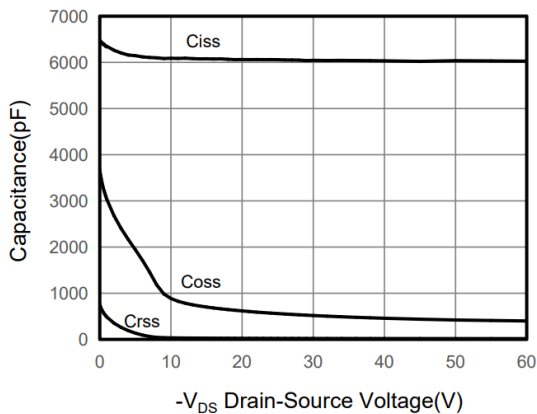


Figure 5. Capacitance

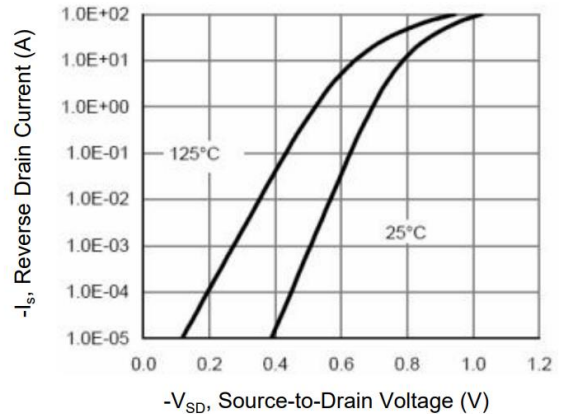


Figure 6. Source-Drain Diode Forward

Typical Characteristics

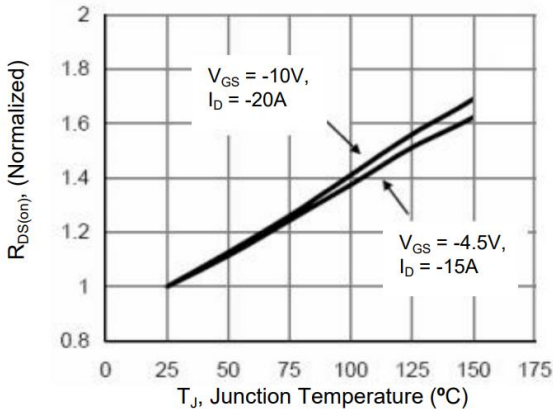


Figure 7. Drain-Source On-Resistance

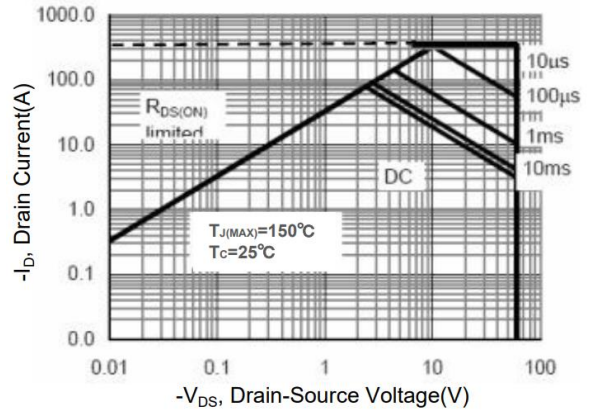


Figure 8. Safe Operation Area

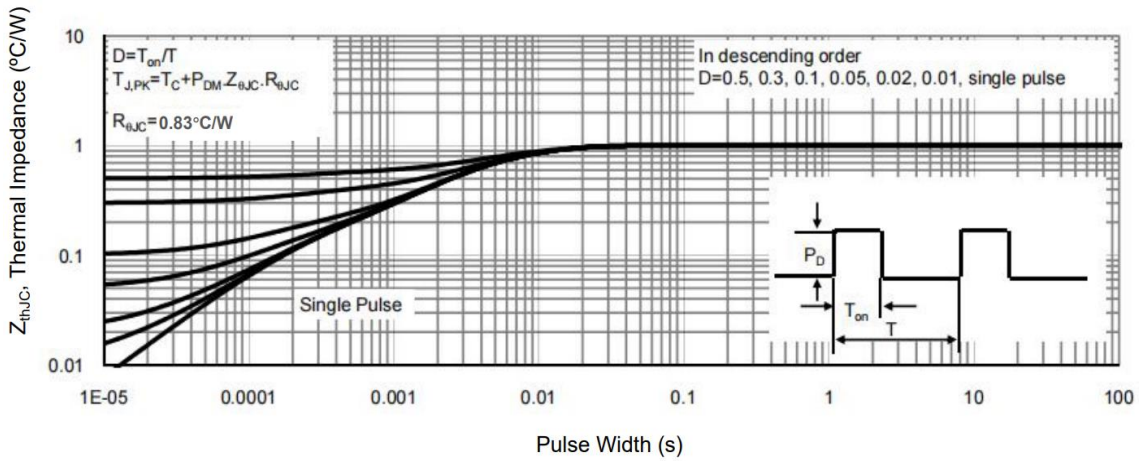
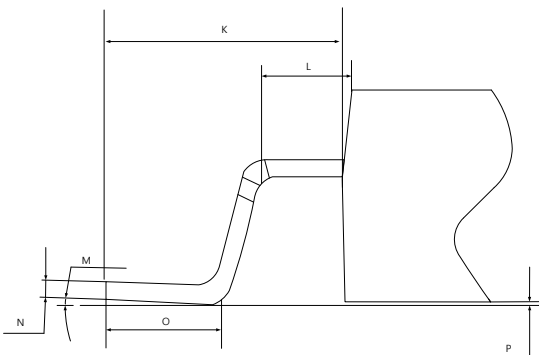
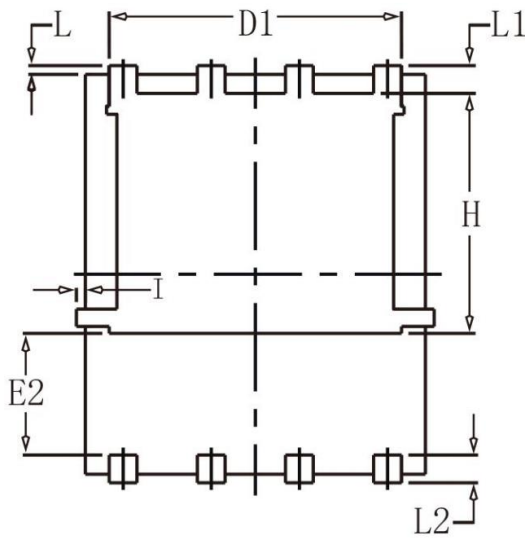
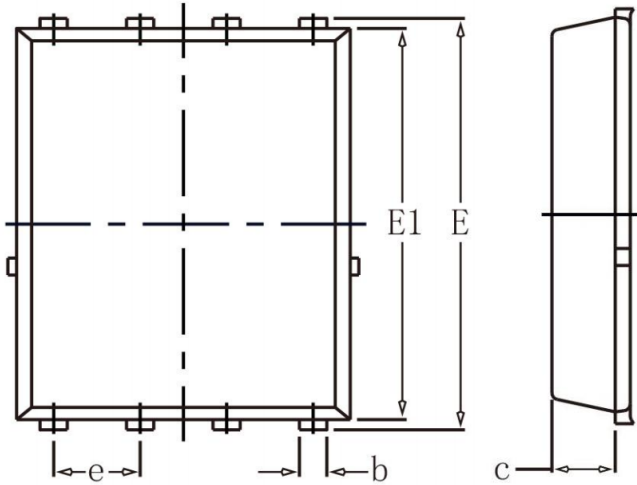


Figure 9. Normalized Maximum Transient Thermal Impedance



Package Outline

Unit : mm



SYMBOL	DIMENSIONS		
	MIN	NOM	MAX
A	1.03	-	1.17
b	0.34	-	0.48
c	0.824	-	0.970
D	4.80	-	5.40
D1	4.11	-	4.31
D2	4.80	-	5.00
E	5.59	-	6.15
E1	5.65	-	5.85
E2	1.60	-	-
e	1.27 BSC		
L	0.05	-	0.25
L1	0.38	-	0.50
L2	0.38	-	0.50
H	3.30	-	3.50
I	-	-	0.18

