

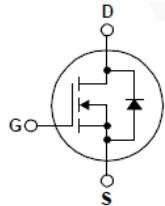
# RTK4N65D

RTK4N65D – N channel Si MOSFET



## Features

- Low gate charge
- Low capacitance fast switching
- Halogen free, RoHS Compliant
- 100% UIS tested

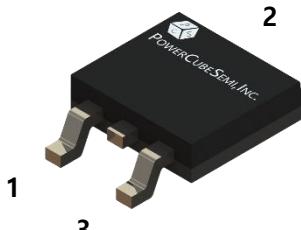


## Applications

- High frequency switching mode power supply
- Electronic ballast
- LED Power supply

## Pin Description

- 1 : Gate  
2 : Drain  
3 : Source



PKG type : TO-252

## Absolute Maximum Ratings $T_C=25^\circ\text{C}$ Unless Otherwise Noted

Symbol	Parameter		Value	Unit
$V_{DSS}$	Drain to Source Voltage		650	V
$V_{GSS}$	Gate to Source Voltage		$\pm 30$	
$T_J$	Maximum Junction Temperature		150	°C
$T_{STG}$	Storage Temperature Range		-55 to 150	°C
$I_S$	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	4	A
$I_{D, \text{pulse}}$	250us Pulse Drain Current Tested	$T_C=25^\circ\text{C}$	16	A
$I_D$	Continuous Drain Current ( $V_{GS}=10\text{V}$ )	$T_C=25^\circ\text{C}$	4	A
		$T_C=100^\circ\text{C}$	2.5	
$P_D$	Power dissipation	$T_C=25^\circ\text{C}, T_J=150^\circ\text{C}$	77	W
$E_{AS}$	Avalanche Energy, Single Pulsed		198	mJ
$I_{AS}$	Avalanche Current, Single Pulsed		6.3	A



## Static Characteristics

T<sub>j</sub>=25°C unless otherwise specified

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	650	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V	-	-	1	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	-	4	V
R <sub>DS(ON)</sub>	Static Drain-Source on state resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2A	-	2.5	2.7	Ω

## Dynamic Characteristics

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
C <sub>iss</sub>	Input capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	-	600	-	pF
C <sub>oss</sub>	Output capacitance		-	55	-	
C <sub>rss</sub>	Reverse transfer capacitance		-	3.2	-	
t <sub>d(on)</sub>	Turn-on Delay time	V <sub>DS</sub> =325V, I <sub>D</sub> =4A, V <sub>GS</sub> =15V, R <sub>G</sub> =10Ω	-	12	-	ns
T <sub>r</sub>	Turn-on Rise time		-	31	-	
t <sub>d(off)</sub>	Turn-off Delay time		-	42	-	
T <sub>f</sub>	Turn-off Fall time		-	15	-	

## Gate Charge Characteristics

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
Q <sub>g(tot)</sub>	Total gate charge at 10V	V <sub>DS</sub> =520V, I <sub>D</sub> =4A V <sub>GS(on)</sub> =10V	-	12	-	nC
Q <sub>gs</sub>	Gate to source gate charge		-	3.2	-	
Q <sub>gd</sub>	Gate to drain "Miller" charge		-	5.1	-	



## Diode Characteristics

Symbol	Parameter	Test Condition	Numerical		Unit
			Typ.	Max.	
$I_S$	Maximum continuous drain to source diode forward current		-	4	A
$I_{SM}$	Maximum pulsed drain to source diode forward current		-	16	A
$V_{SD}$	Drain to source diode forward voltage	$I_{SD}=4A, V_{GS} = 0V$	0.85	1.5	V
$T_{rr}$	Reverse recovery time	$I_F=4A, V_R=400V,$ $-dI_F/dt=100A/\mu s$	282	-	ns
$Q_{rr}$	Reverse recovery charge		1.4	-	$\mu C$
$I_{rrm}$	Reverse recovery current		10	-	A

## Thermal Characteristics

Symbol	Parameter	Numerical	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.62	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	110	

## NOTE

- 1 : Pulse width limited by safe operating area.
- 2 : Calculated continuous current based on maximum allowable junction temperature.
- 3 : Limited by  $T_{Jmax}$ ,  $L=10mH$ ,  $V_{DD}=50V$ ,  $R_G=25\Omega$ , Starting  $T_J=25^{\circ}C$ .
- 4 : Pulse test; Pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
- 5 : Guaranteed by design, not subjected to production test.

# Typical Characteristics

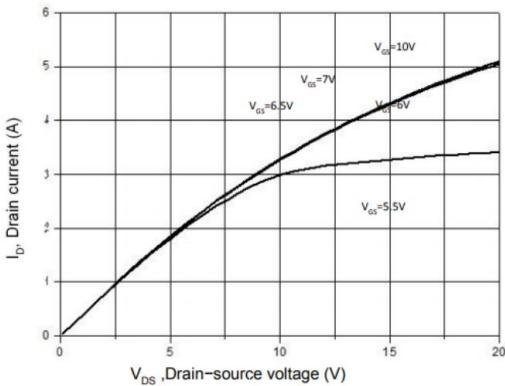


Figure 1. Typical output characteristics

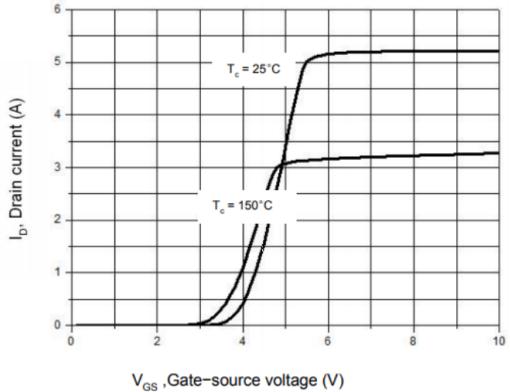


Figure 2. Transfer Characteristics

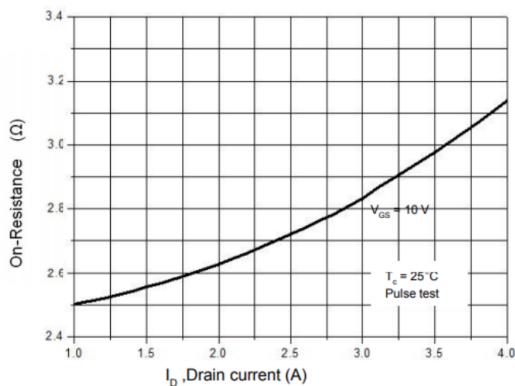


Figure 3. On-resistance variation vs. Drain current

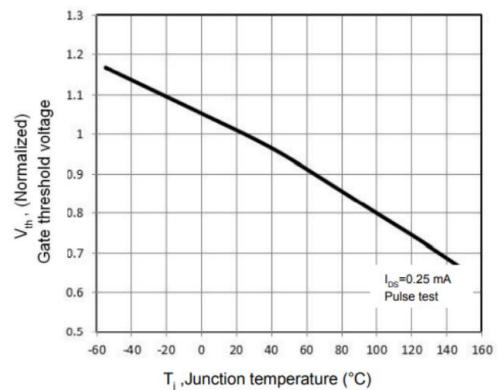


Figure 4. Threshold voltage vs. Temperature

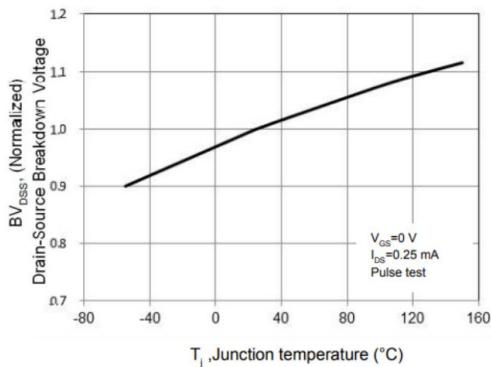


Figure 5. Breakdown Voltage vs. Temperature

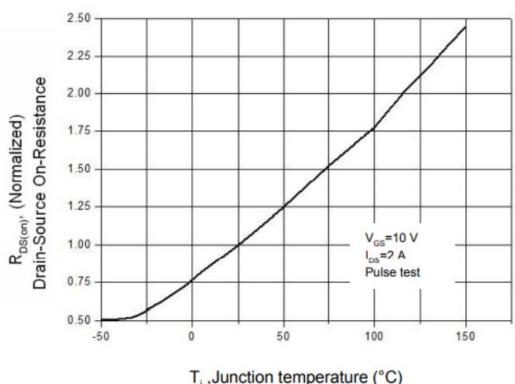


Figure 6. On-resistance vs. Temperature

# Typical Characteristics

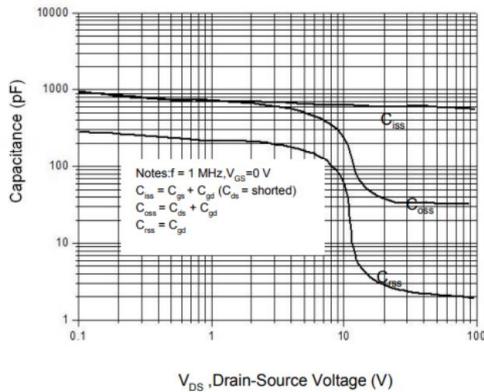


Figure 7. Capacitance Characteristics

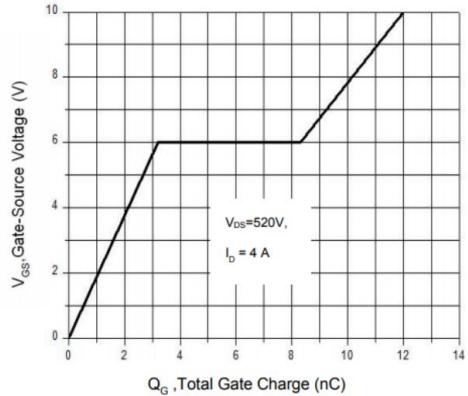


Figure 8. Gate charge characteristics

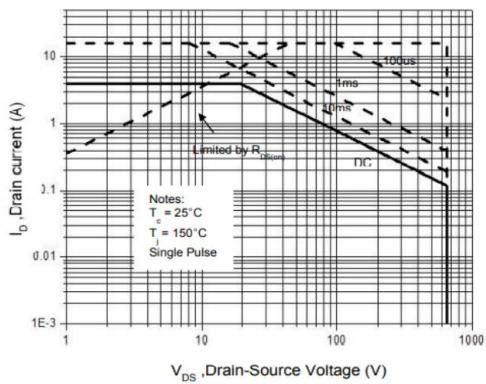


Figure 9. Maximum Safe Operating Area

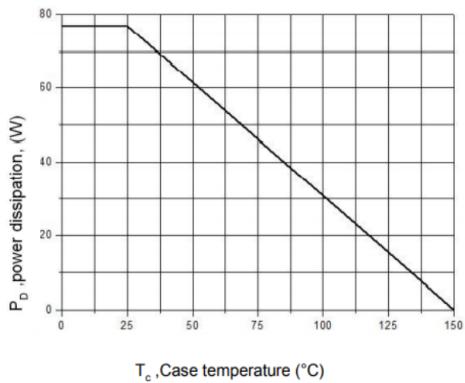


Figure 10. Power dissipation vs. Temperature

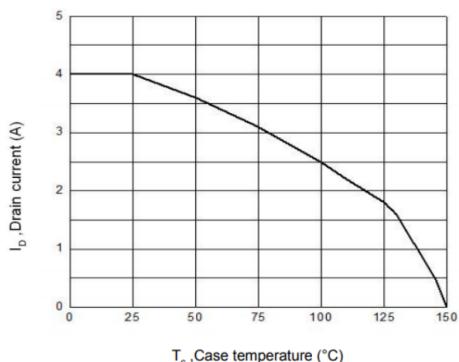


Figure 11. Continuous Drain Current vs. Temperature

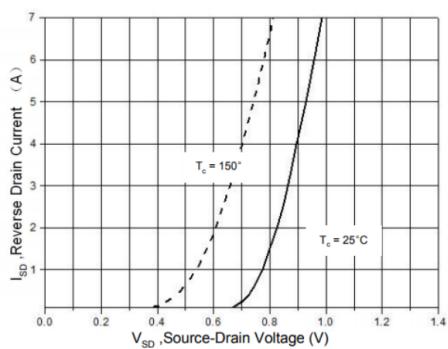
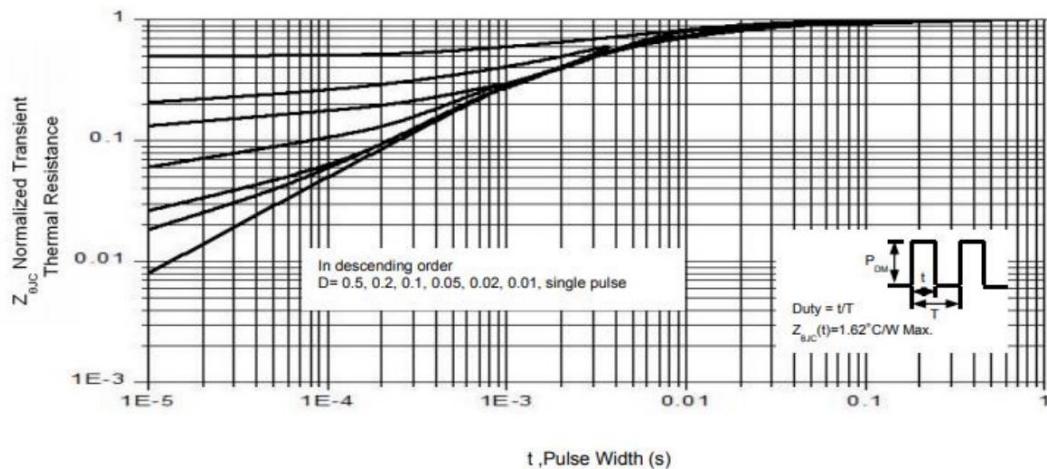


Figure 12. Body diode transfer characteristics

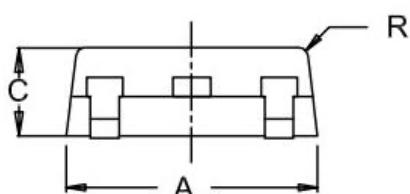
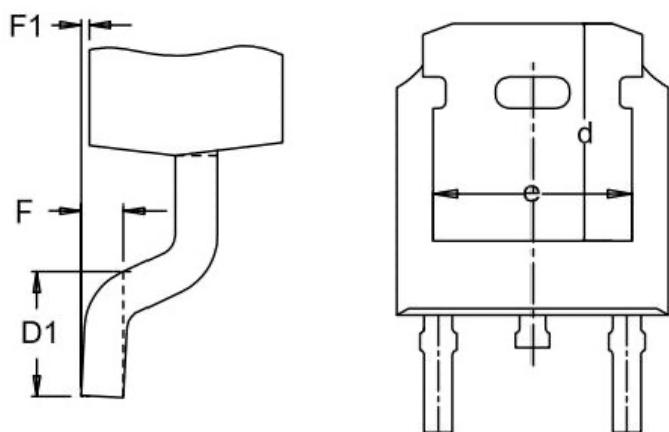
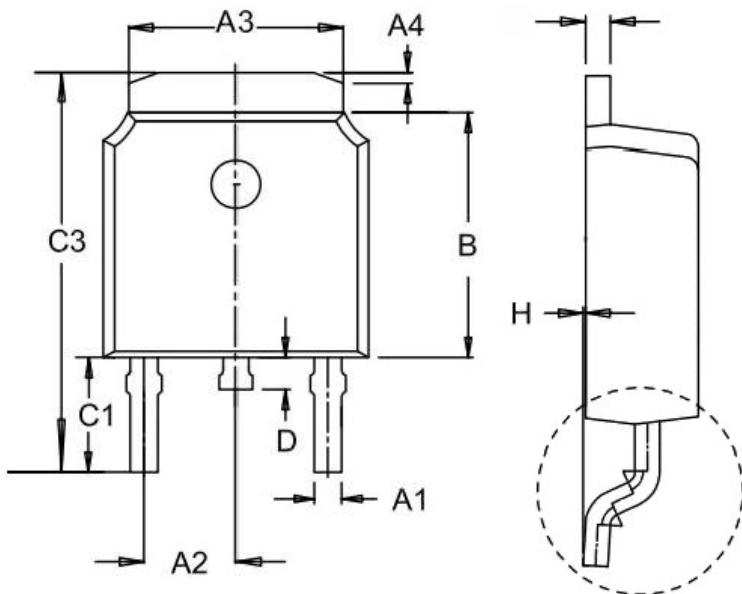
## Typical Characteristics



**Figure 13. Transient Thermal Impedance**

## Package Outline

Unit : mm



SYMBOL	DIMENSIONS		
	MIN	NOM	MAX
<b>A</b>	6.55	6.60	6.65
<b>A1</b>	0.64	0.69	0.74
<b>A2</b>	-	2.286	-
<b>A3</b>	5.234	5.334	5.434
<b>A4</b>	0.07	0.27	0.47
<b>B</b>	6.05	6.10	6.15
<b>C</b>	2.25	2.30	2.35
<b>C1</b>	2.65	2.78	2.95
<b>C2</b>	0.504	0.508	0.510
<b>C3</b>	9.75	9.85	10.0
<b>D</b>	0.70	0.80	0.90
<b>D1</b>	1.40	1.50	1.60
<b>F</b>	-	0.508	-
<b>F1</b>	0	0.05	0.10
<b>H</b>	0	0.05	0.10
<b>R</b>	-	0.25	-