

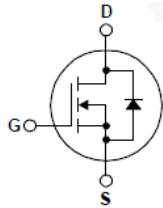
RTK7N65F

RTK7N65F – N channel Si MOSFET



Features

- Low gate charge
- Low capacitance fast switching
- Halogen free, RoHS Compliant
- 100% UIS tested

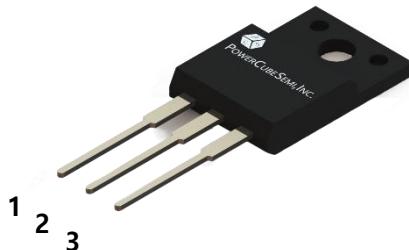


Applications

- High frequency switching mode power supply
- Electronic ballast
- LED Power supply

Pin Description

- 1 : Gate
2 : Drain
3 : Source



PKG type : TO-220F

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ Unless Otherwise Noted

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage	$T_C=25^\circ\text{C}$	650	V
V_{GSS}	Gate to Source Voltage		± 30	
T_J	Maximum Junction Temperature		150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range		-55 to 150	$^\circ\text{C}$
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	7	A
$I_{D, \text{pulse}}$	250us Pulse Drain Current Tested	$T_C=25^\circ\text{C}$	28	A
I_D	Continuous Drain Current ($V_{GS}=10\text{V}$)	$T_C=25^\circ\text{C}$	7	A
		$T_C=100^\circ\text{C}$	4.3	
P_D	Power dissipation	$T_C=25^\circ\text{C}, T_J=150^\circ\text{C}$	39	W
E_{AS}	Avalanche Energy, Single Pulsed		352	mJ
I_{AS}	Avalanche Current, Single Pulsed		8.4	A



Static Characteristics

T_j=25°C unless otherwise specified

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	650	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D = 250μA	2	-	4	V
R _{DS(ON)}	Static Drain-Source on state resistance	V _{GS} = 10V, I _D = 3.5A	-	1.2	1.4	Ω

Dynamic Characteristics

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
C _{iss}	Input capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	1090	-	pF
C _{oss}	Output capacitance		-	111	-	
C _{rss}	Reverse transfer capacitance		-	6.1	-	
t _{d(on)}	Turn-on Delay time	V _{DS} =325V, I _D =7A, V _{GS} =15V, R _G =10Ω	-	12.2	-	ns
T _r	Turn-on Rise time		-	33.4	-	
t _{d(off)}	Turn-off Delay time		-	53.6	-	
T _f	Turn-off Fall time		-	15	-	

Gate Charge Characteristics

Symbol	Parameter	Test Condition	Numerical			Unit
			Min	Typ.	Max.	
Q _{g(tot)}	Total gate charge at 10V	V _{DS} =520V, I _D =7A V _{GS(on)} =10V	-	20.7	-	nC
Q _{gs}	Gate to source gate charge		-	5.7	-	
Q _{gd}	Gate to drain "Miller" charge		-	7.2	-	



Diode Characteristics

Symbol	Parameter	Test Condition	Numerical		Unit
			Typ.	Max.	
I_S	Maximum continuous drain to source diode forward current		-	7	A
I_{SM}	Maximum pulsed drain to source diode forward current		-	28	A
V_{SD}	Drain to source diode forward voltage	$I_{SD}=7A, V_{GS} = 0V$	0.85	1.5	V
T_{rr}	Reverse recovery time	$I_F=7A, V_R=325V,$ $-dI_F/dt=100A/\mu s$	373.2	-	ns
Q_{rr}	Reverse recovery charge		2.1	-	μC
I_{rrm}	Reverse recovery current		15	-	A

Thermal Characteristics

Symbol	Parameter	Numerical	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.2	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		

NOTE

- 1 : Pulse width limited by safe operating area.
- 2 : Calculated continuous current based on maximum allowable junction temperature.
- 3 : Limited by T_{Jmax} , $L=10mH$, $V_{DD}=50V$, $R_G=25\Omega$, Starting $T_J=25^{\circ}C$.
- 4 : Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- 5 : Guaranteed by design, not subjected to production test.

Typical Characteristics

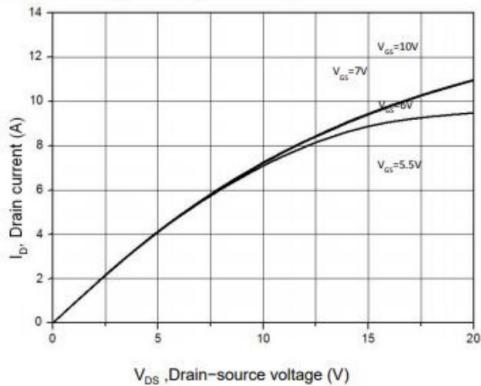


Figure 1. Typical output characteristics

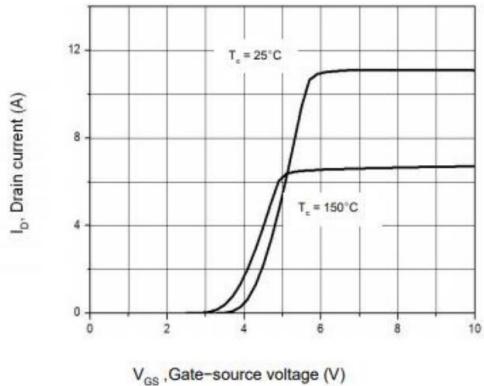


Figure 2. Transfer Characteristics

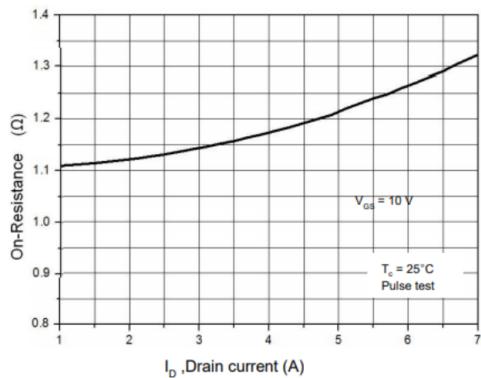


Figure 3. On-resistance variation vs. Drain current

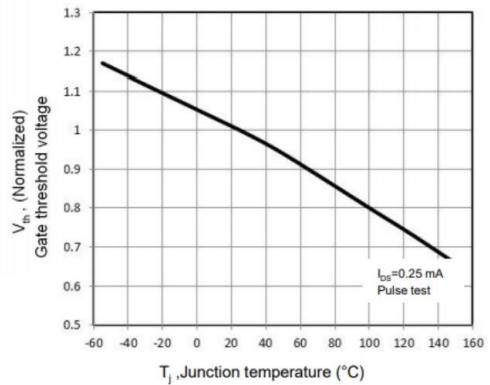


Figure 4. Threshold voltage vs. Temperature

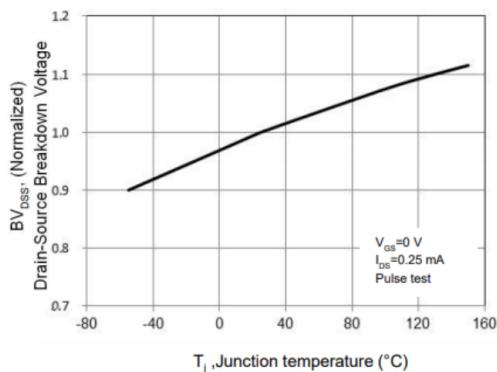


Figure 5. Breakdown Voltage vs. Temperature

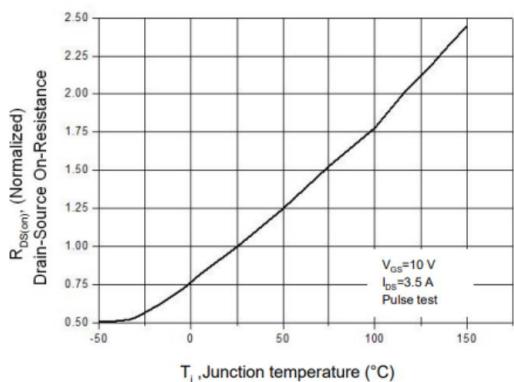


Figure 6. On-resistance vs. Temperature

Typical Characteristics

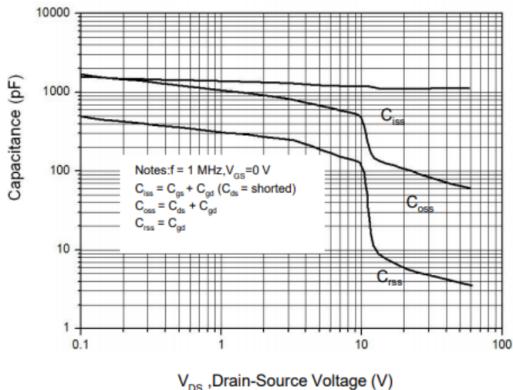


Figure 7. Capacitance Characteristics

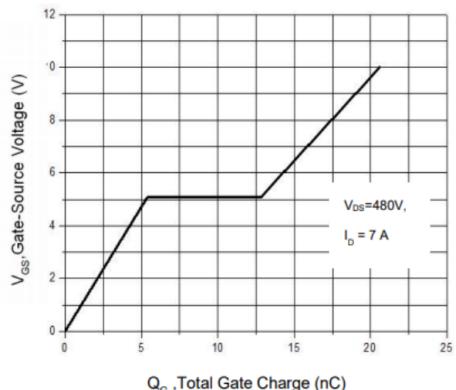


Figure 8. Gate charge characteristics

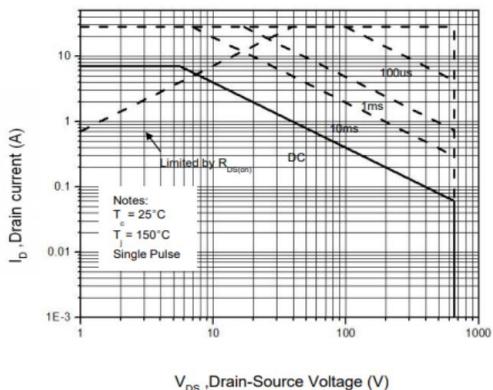


Figure 9. Maximum Safe Operating Area

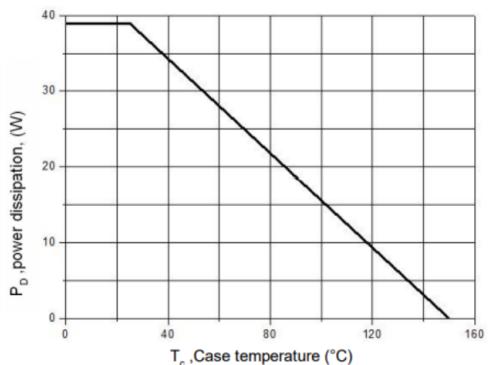


Figure 10. Power dissipation vs. Temperature

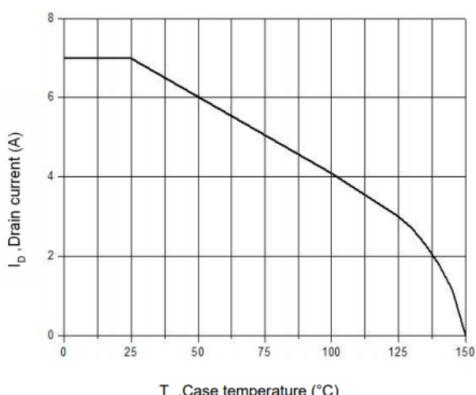


Figure 11. Continuous Drain Current vs. Temperature

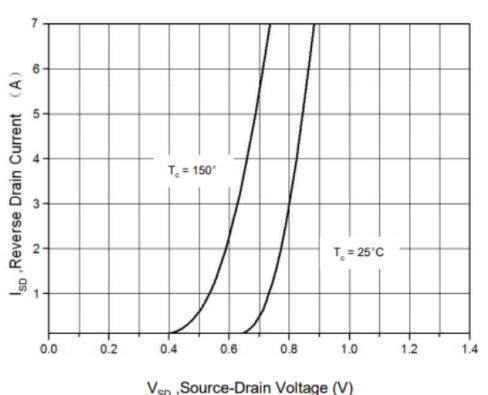


Figure 12. Body diode Transfer Characteristics

Typical Characteristics

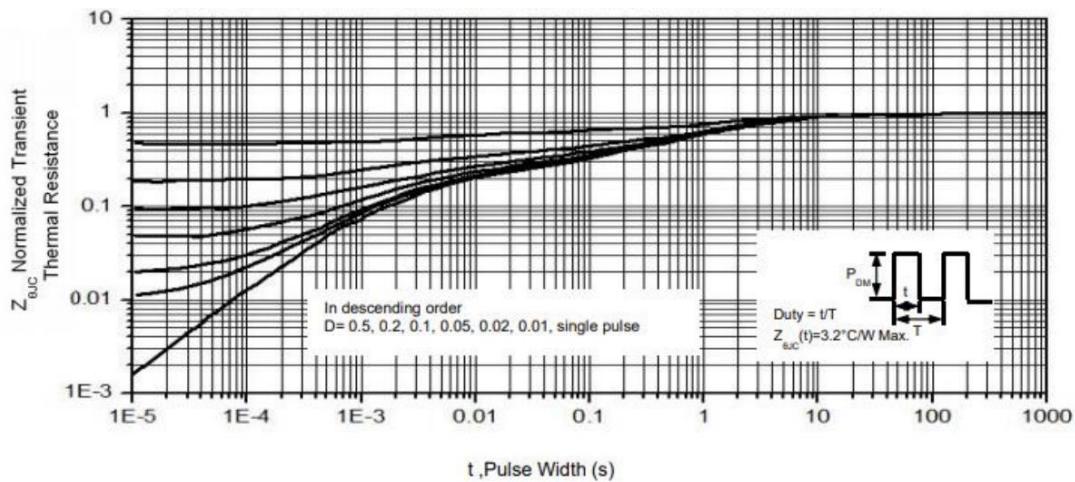


Figure 13. Transient Thermal Impedance

Package Outline

Front View Dimensions:

- Total width **A**: 3-Φ1.5深0.15
- Total height **B**: 3.43±0.10
- Top surface radius **R0.5**
- Bottom surface note: 光面深0.02
- Side wall thicknesses: **h1**, **h2**, **h3**, **h4**, **h5**
- Bottom plate thicknesses: **L**, **C**, **D1**, **D2**
- Bottom plate hole diameter: 3-F
- Bottom plate hole depth: 3-F1
- Bottom plate hole diameter: 3-E
- Bottom plate hole depth: 3-F2

Side View Dimensions:

- Side wall thicknesses: **A1**, **A2**
- Side wall angle: 1.0*45°
- Bottom plate thickness: **H**
- Bottom plate angle: 3-F3
- Bottom plate angle: 3-F4

Bottom View Dimensions:

- Bottom plate width: **G**
- Bottom plate height: **H6**
- Bottom plate hole diameter: Φ3.18+0.10
- Bottom plate hole diameter: 3-Φ1.5深0.15
- Bottom plate hole diameter: 3-F2

SYMBOL	DIMENSIONS		
	MIN	NOM	MAX
A	10.00	10.20	10.40
A1	-	0.70	-
A2	2.35	2.55	2.75
B	15.80	15.90	16.00
C	13.00	13.25	13.50
D1	2.54 BSC		
D2	5.08 BSC		
E	1.27	1.32	1.40
F	1.25	1.28	1.30
F1	0.75	0.80	0.85
F2	0.35	0.40	0.50
G	6.90	7.00	7.10
H	2.66	2.76	2.86
h1	3.20	3.30	3.40
h2	1.70	1.80	1.90
h3	2.00	2.10	2.20
h4	6.70	6.79	6.90
h5	9.30	9.41	9.50
h6	10.44	10.54	10.64
I	0.40	0.50	0.60
K	4.60	4.70	4.80
L	2.90	3.00	3.10